

# RJP65S05DWA / RJP65S05DWS

650V - 75A - IGBT

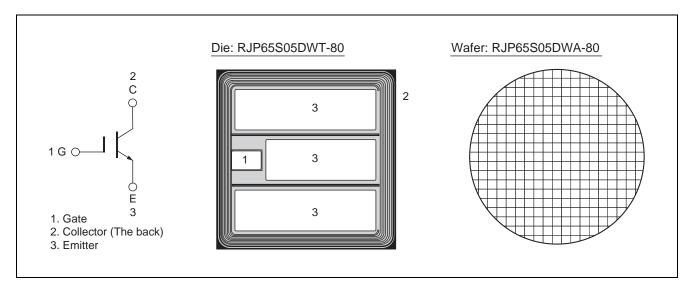
Application: Inverter

R07DS0822EJ0400
Rev.4.00
Nov. 06, 2015

### **Features**

- Low collector to emitter saturation voltage  $V_{CE(sat)} = 1.5 \text{ V}$  typ. (at  $I_C = 75 \text{ A}$ ,  $V_{GE} = 15 \text{ V}$ ,  $Tc = 25 ^{\circ}\text{C}$ )
- High speed Switching
- Short circuit withstands time (10 µs min.)

### **Outline**



### **Absolute Maximum Ratings**

 $(Tc = 25^{\circ}C \text{ unless otherwise noted})$ 

Item		Symbol	Ratings	Unit
Collector to emitter voltage		Vces	650	V
Gate to emitter voltage		V <sub>GES</sub>	±30	V
Collector current	Tc = 25°C	Ic	150	Α
	Tc = 100°C	Ic	75	Α
Junction temperature		Tj	175 Note1	°C

Notes: 1. Please use this device in the thermal conditions where the junction temperature does not exceed 175°C. IGBT Application Note is disclosed about reliability test and application condition up to Tj = 175°C.

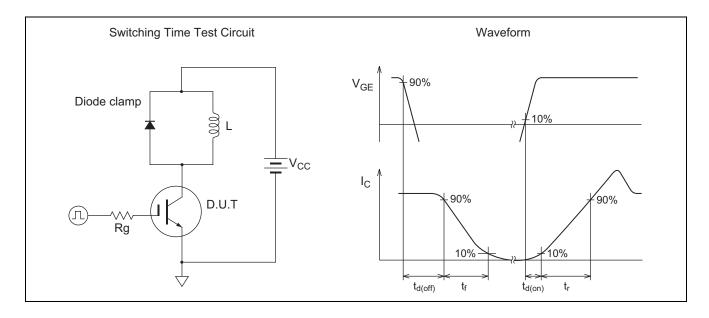
# **Electrical Characteristics** (Datas below are measured values on a package configuration.)

 $(Tc = 25^{\circ}C \text{ unless otherwise noted})$ 

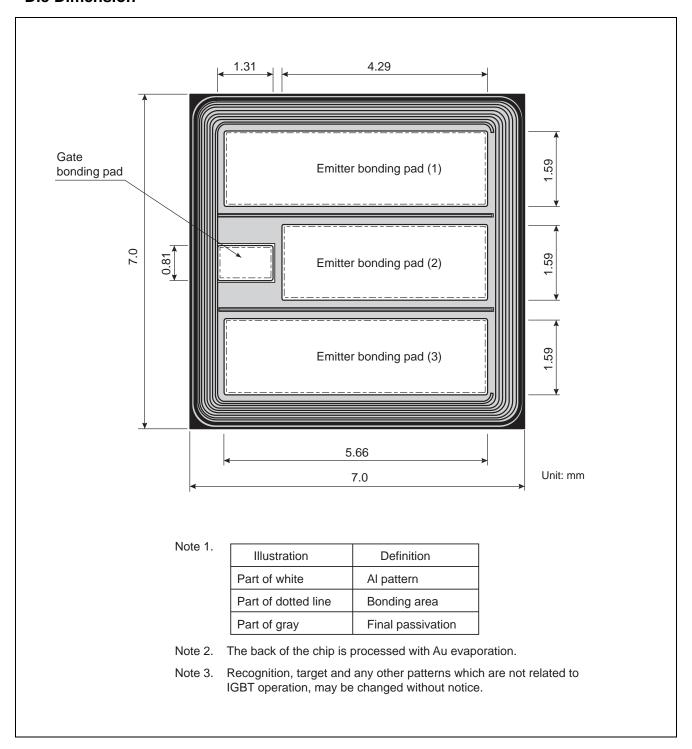
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage collector current	I <sub>CES</sub>	_	_	1	μΑ	$V_{CE} = 650 \text{ V}, V_{GE} = 0$
Gate to emitter leak current	I <sub>GES</sub>	_	_	±1	μΑ	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$
Gate to emitter cutoff voltage	$V_{GE(off)}$	5.0	_	6.8	V	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 1.5mA
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>		1.50	1.80	V	Ic = 75 A, V <sub>GE</sub> = 15 V Note2
Input capacitance	Cies		6600	_	pF	V <sub>CE</sub> = 25 V V <sub>GE</sub> = 0 f = 1 MHz
Output capacitance	Coes	_	280	_	pF	
Reveres transfer capacitance	Cres	_	220	_	pF	
Total gate charge	Qg	_	350	_	nC	V <sub>GE</sub> = 15 V V <sub>CE</sub> = 300 V I <sub>C</sub> = 75 A
Gate to emitter charge	Qge	_	65	_	nC	
Gate to collector charge	Qgc		200	_	nC	
Switching time Note3	t <sub>d(on)</sub>	_	40	_	ns	$V_{CC}$ = 300 V $I_{C}$ = 75 A $V_{GE}$ = ±15 V $Rg$ = 10 Ω, $T_{C}$ = 150 °C Inductive load
	tr	_	50	_	ns	
	t <sub>d(off)</sub>	_	270	_	ns	
	t <sub>f</sub>		75	_	ns	
Short circuit withstand time Note4	t <sub>sc</sub>	10	_	_	μS	$V_{CC} \le 360 \text{ V}$ , $V_{GE} = 15 \text{ V}$ $T_{C} = 150 \text{ °C}$

Notes: 2. Pulse test.

- 3. Switching time test circuit and waveform are shown below.
- 4. Verified by design.



### **Die Dimension**



# **Ordering Information**

Orderable Part Number	Shipment form			
RJP65S05DWA-80#W0	Unsawn wafer			
RJP65S05DWS-80#W0	Sawn wafer			

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